

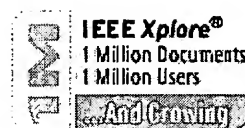
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L8	0	thermometer binary gate adders	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/11/18 12:49
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L21	1	thermometer binary multiplexor	USPAT	SAME	OFF	2004/11/18 14:47
L9	2	thermometer binary gates adder	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	SAME	OFF	2004/11/18 12:54
L4	2	thermometer binary SAME adder SAME gate	US-PGPUB; USOCR; EPO; JPO; DERWENT	WITH	OFF	2004/11/18 12:33
L5	2	thermometer binary SAME adder SAME gate	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	OFF	2004/11/18 12:34
L15	7	thermometer binary AND gates adder	USPAT	SAME	OFF	2004/11/18 13:25
L23	8	thermometer binary adding	USPAT	SAME	OFF	2004/11/18 14:57
L3	8	thermometer code binary SAME adder	US-PGPUB; USOCR; EPO; JPO; DERWENT	WITH	OFF	2004/11/18 12:32
L2	9	thermometer code binary SAME adder	USPAT	WITH	OFF	2004/11/18 12:24
L22	19	thermometer binary "or"	USPAT	SAME	OFF	2004/11/18 14:57
L18	22	thermometer binary AND adders	USPAT	WITH	OFF	2004/11/18 13:43
L6	25	thermometer binary SAME gate	US-PGPUB; USOCR; EPO; JPO; DERWENT; IBM_TDB	WITH	OFF	2004/11/18 12:46

L20	27	thermometer ADJ1 binary	USPAT	AND	OFF	2004/11/18 14:43
L19	51	(341/96).CCLS.	USPAT	OR	OFF	2004/11/18 13:54
L17	58	thermometer binary AND adder	USPAT	WITH	OFF	2004/11/18 13:25
L16	73	thermometer binary AND adder	USPAT	SAME	OFF	2004/11/18 13:25
L1	220	thermometer code binary	USPAT	WITH	OFF	2004/11/18 12:18
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**1 Fat tree encoder design for ultra-high speed flash A/D converters***Daegyu Lee; Jincheol Yoo; Kyusun Choi; Ghaznavi, J.;*

Circuits and Systems, 2002. MWSCAS-2002. The 2002 45th Midwest Symposium on , Volume: 2 , 4-7 Aug. 2002

Pages:II-87 - II-90 vol.2

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**2 A self-trimming 14-b 100-MS/s CMOS DAC***Bugeja, A.R.; Bang-Sup Song;*

Solid-State Circuits, IEEE Journal of , Volume: 35 , Issue: 12 , Dec. 2000

Pages:1841 - 1852

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**3 A novel coding scheme for the ROM of parallel ADCs, featuring reduced conversion noise in the case of single bubbles in the thermometer code***Padoan, S.; Boni, A.; Morandi, C.; Venturi, F.;*

Electronics, Circuits and Systems, 1998 IEEE International Conference on , Volume: 2 , 7-10 Sept. 1998

Pages:271 - 274 vol.2

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**4 A 4GS/s 6b flash ADC in 0.13  $\mu$ m CMOS***Paulus, C.; Bluthgen, H.-M.; Low, M.; Sicheneder, E.; Bruls, N.; Courtois, A.; Tiebout, M.; Thewes, R.;*

VLSI Circuits, 2004. Digest of Technical Papers. 2004 Symposium on , 17-19 : 2004

Pages:420 - 423

[\[Abstract\]](#) [\[PDF Full-Text \(363 KB\)\]](#) IEEE CNF

**5 The investigation of mercury presence in human blood: an extrapolation from animal data using neural networks**

*Hashemi, R.R.; Bahar, M.; Tyler, A.A.; Young, J.;*

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Pages:512 - 517

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**6 Wallace tree encoding in folding and interpolation ADCs**

*Pereira, P.; Fernandes, J.R.; Silva, M.M.;*

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[\[Abstract\]](#) [\[PDF Full-Text \(426 KB\)\]](#) IEEE CNF

**7 Clock-jitter induced distortion in high speed CMOS switched-current segmented digital-to-analog converters**

*Gonzalez, J.L.; Alarcon, E.;*

Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposium on , Volume: 1 , 6-9 May 2001

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**8 A 12 b 500 MSample/s current-steering CMOS D/A converter**

*Van Den Bosch, A.; Borremans, M.; Steyaert, M.; Sansen, W.;*

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**9 Efficient FPGA-based multistage two-path decimation filter for noise thermometer**

*Saleh, H.; Zimmermann, E.; Brandenburg, G.; Halling, H.;*

Microelectronics, 2001. ICM 2001 Proceedings. The 13th International Conference on , 29-31 Oct. 2001

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**10 A complete 18-bit audio D/A converter**

*Baginski, P.; Brokaw, P.; Wurcer, S.;*

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#### 11 Characteristics of linear-coded D/A converters

*Wikner, J.J.; Vesterbacka, M.;*

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#### 12 D/A conversion with linear-coded weights

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[\[Abstract\]](#) [\[PDF Full-Text \(204 KB\)\]](#) IEEE CNF

#### 13 A precision CTD microprofiler

*Brown, N.;*

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Pages:270 - 278

[\[Abstract\]](#) [\[PDF Full-Text \(824 KB\)\]](#) IEEE CNF

#### 14 Flash analog-to-digital converter using resonant-tunneling multiple valued circuits

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#### 15 A 14 b 150 Msample/s update rate $Q^2$ random walk CMOS DAC

*Vandenbussche, J.; Van der Plas, G.; Van den Bosch, A.; Daems, W.; Gielen, Steyaert, M.; Sansen, W.;*

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Neural Networks, 1999. IJCNN '99. International Joint Conference on , Volume 2 , 10-16 July 1999

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